

FIG. 1
(PRIOR ART)

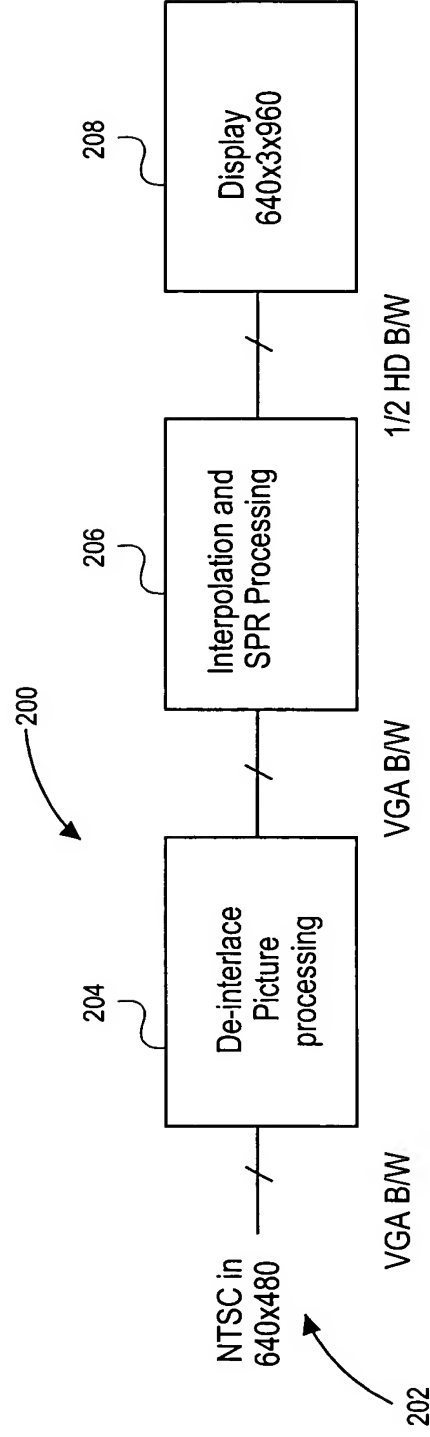
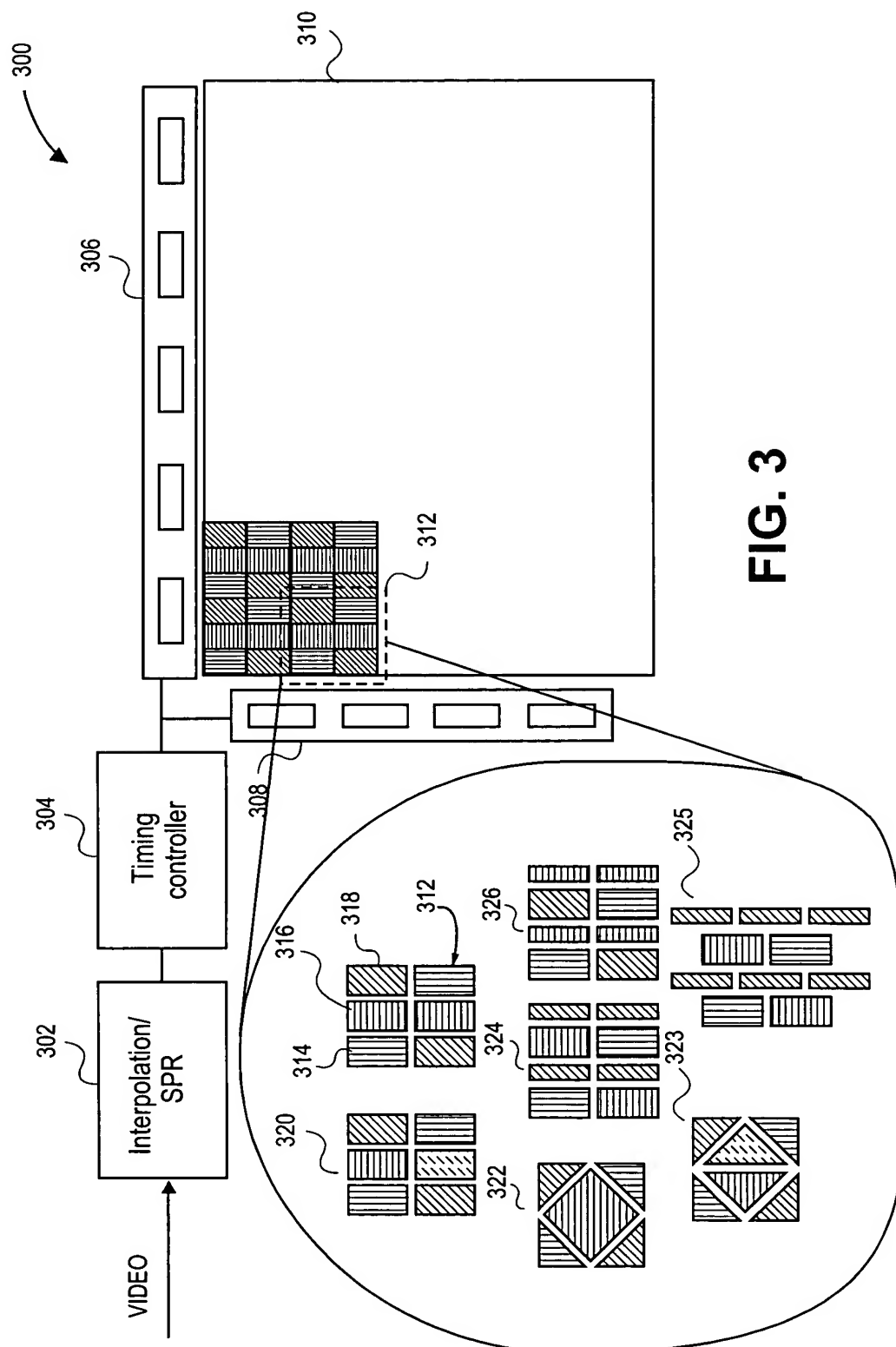


FIG. 2



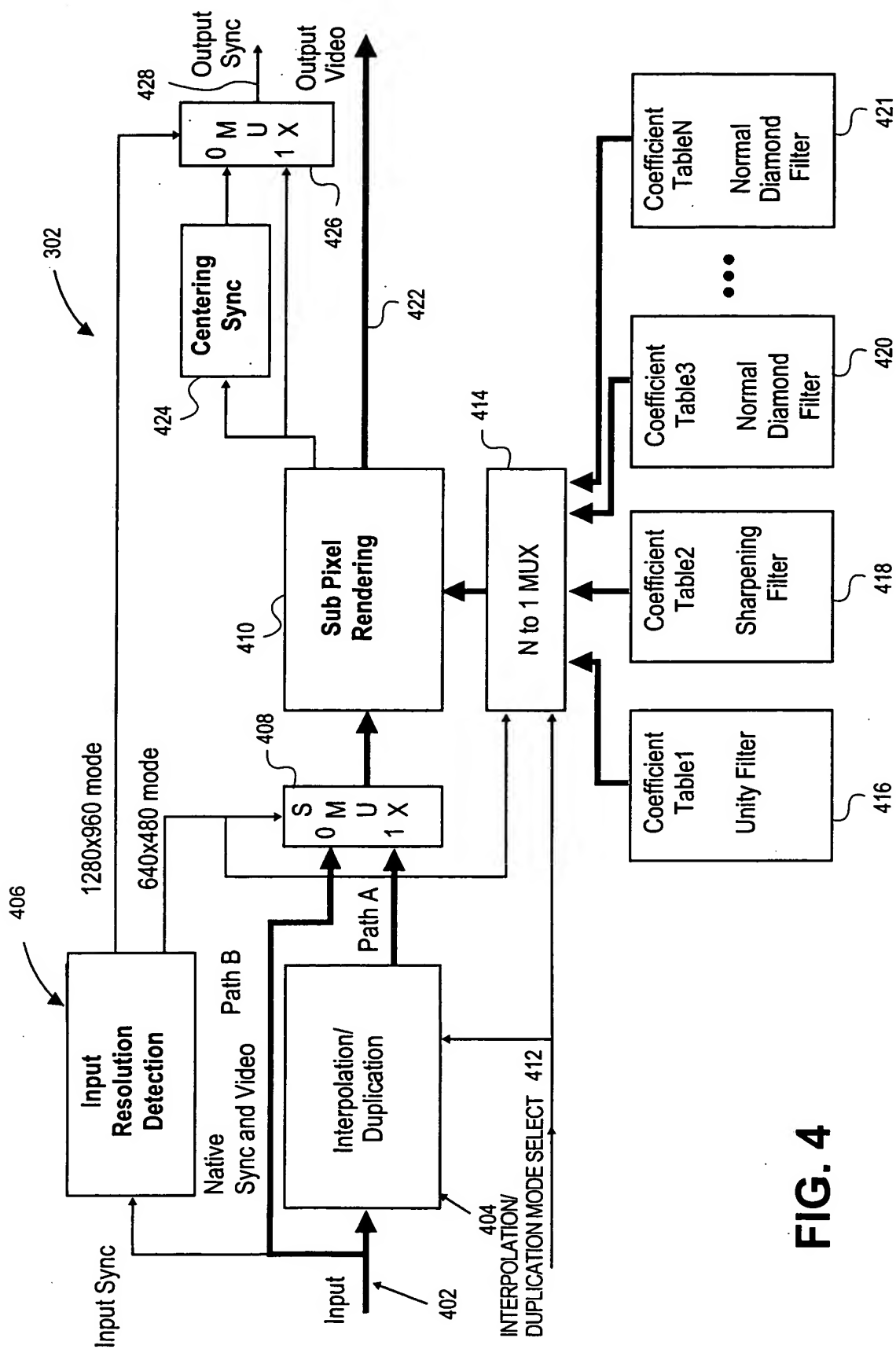


FIG. 4

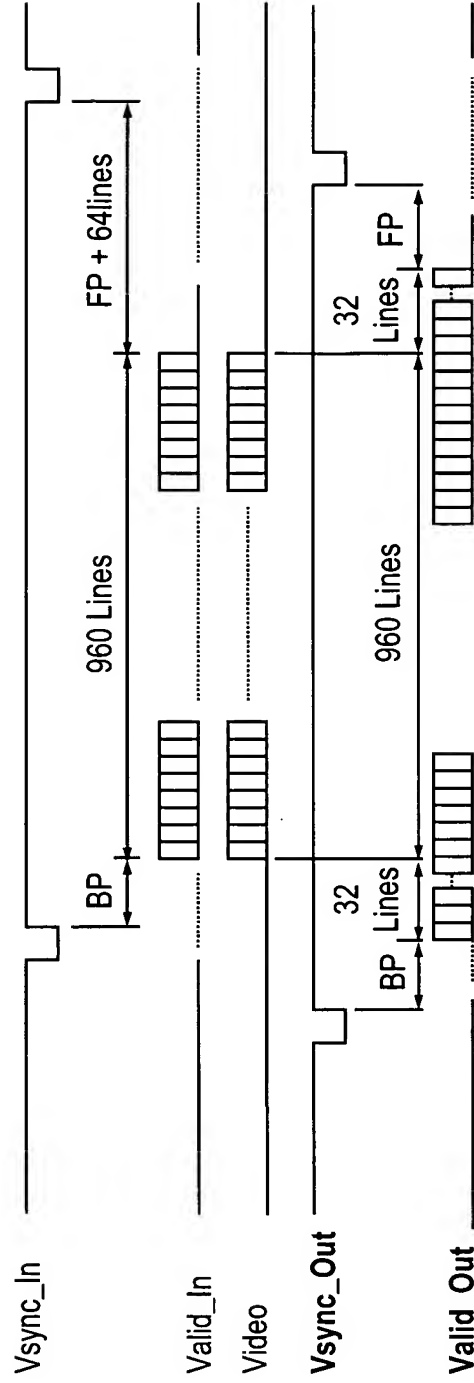


FIG. 5

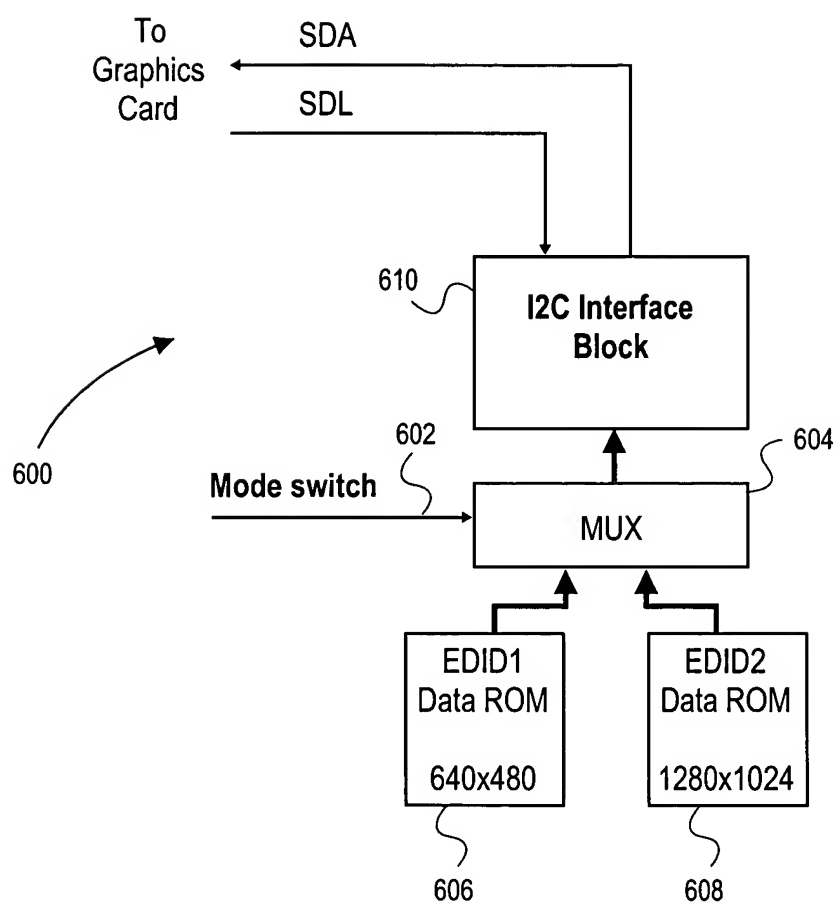


FIG. 6

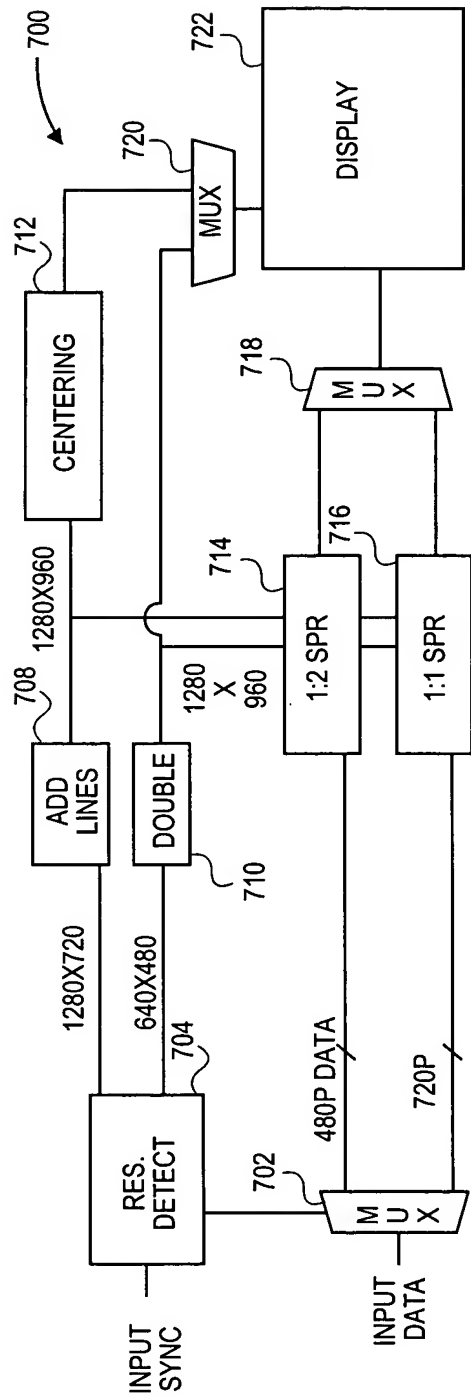


FIG. 7

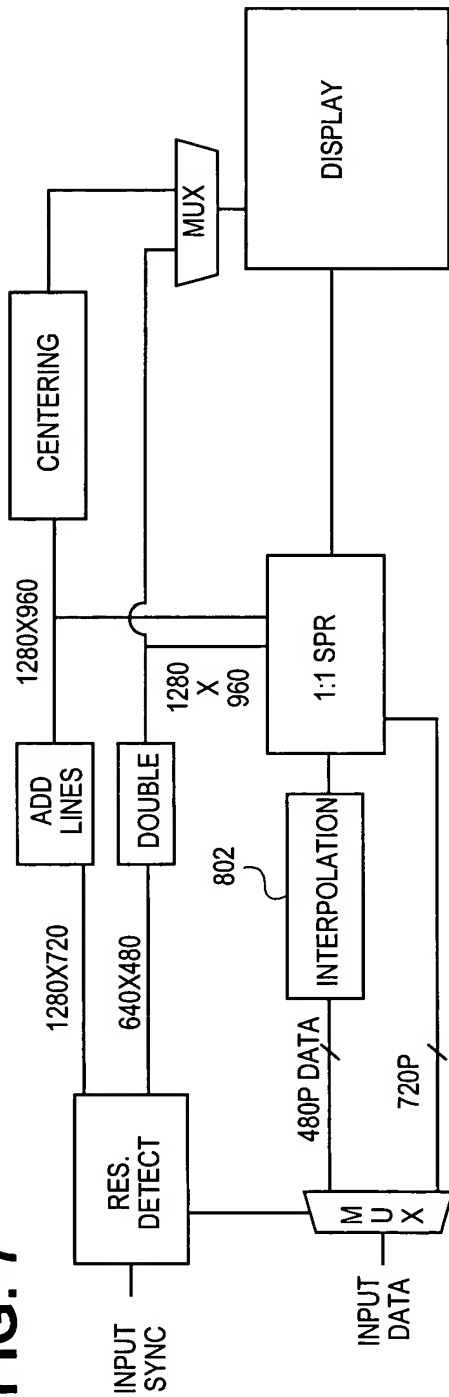


FIG. 8

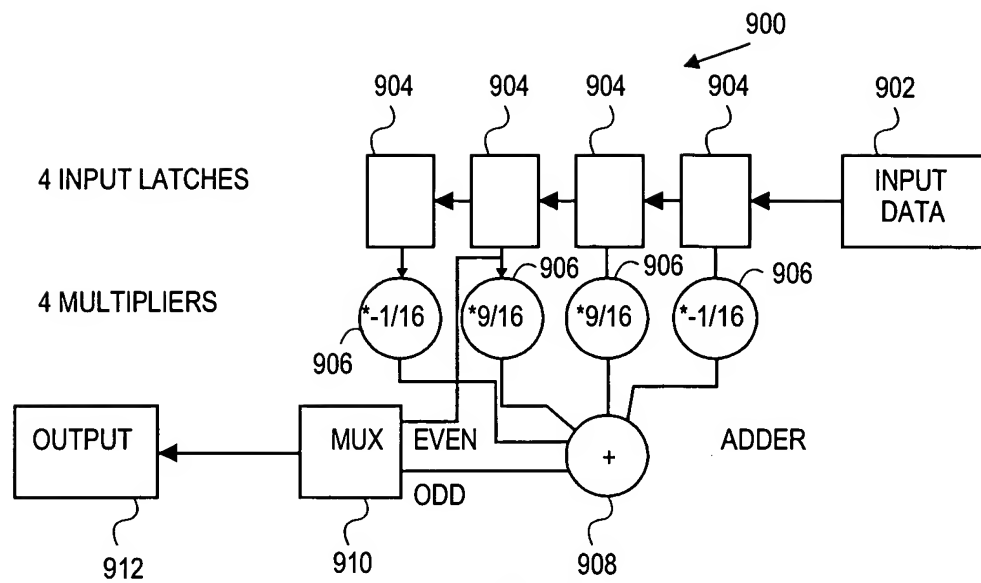


FIG. 9

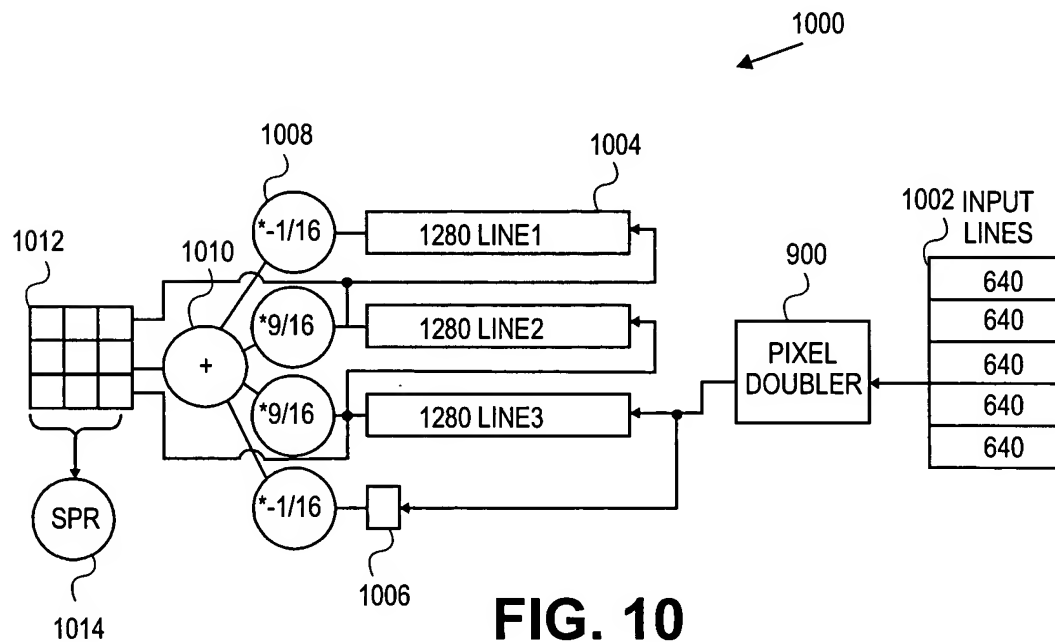


FIG. 10

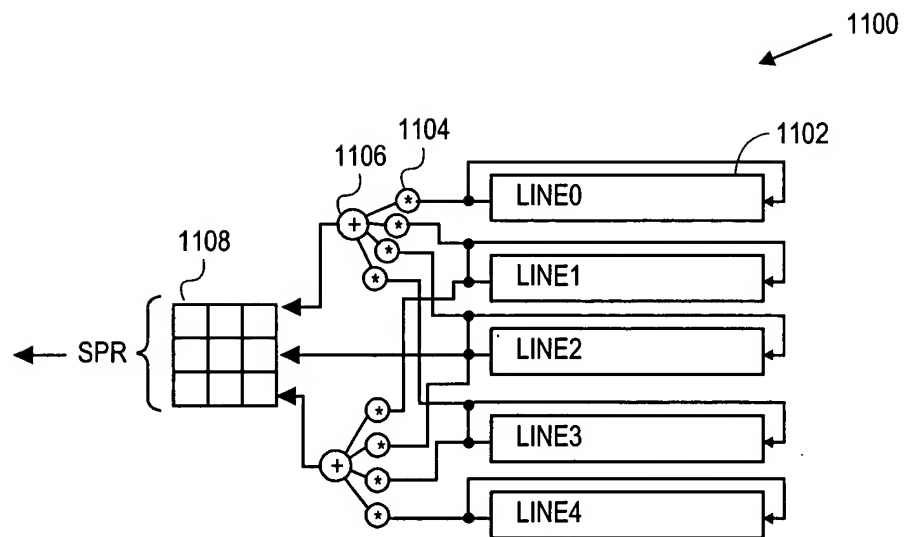


FIG. 11

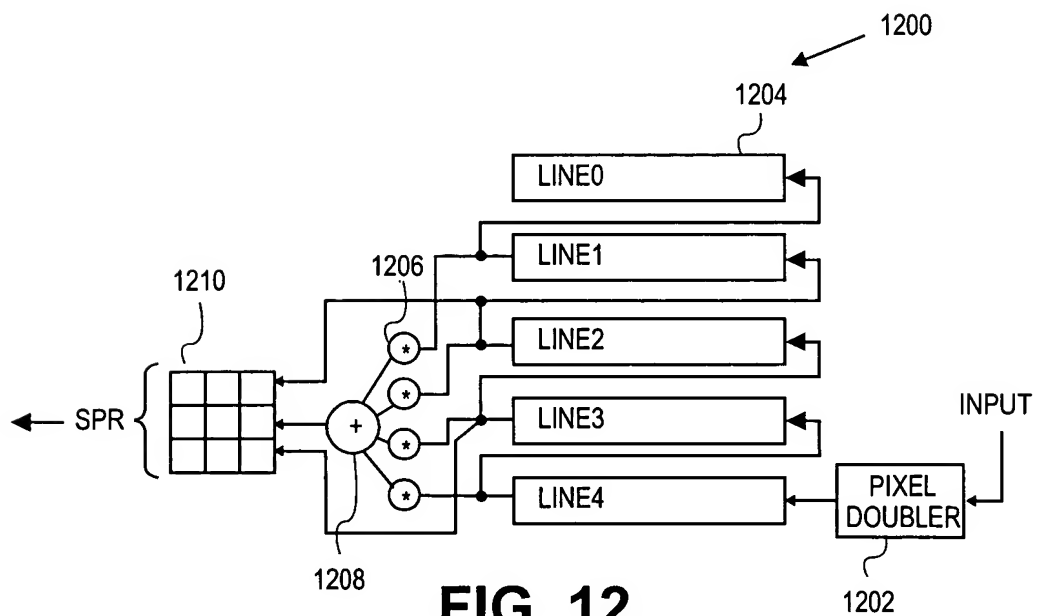


FIG. 12

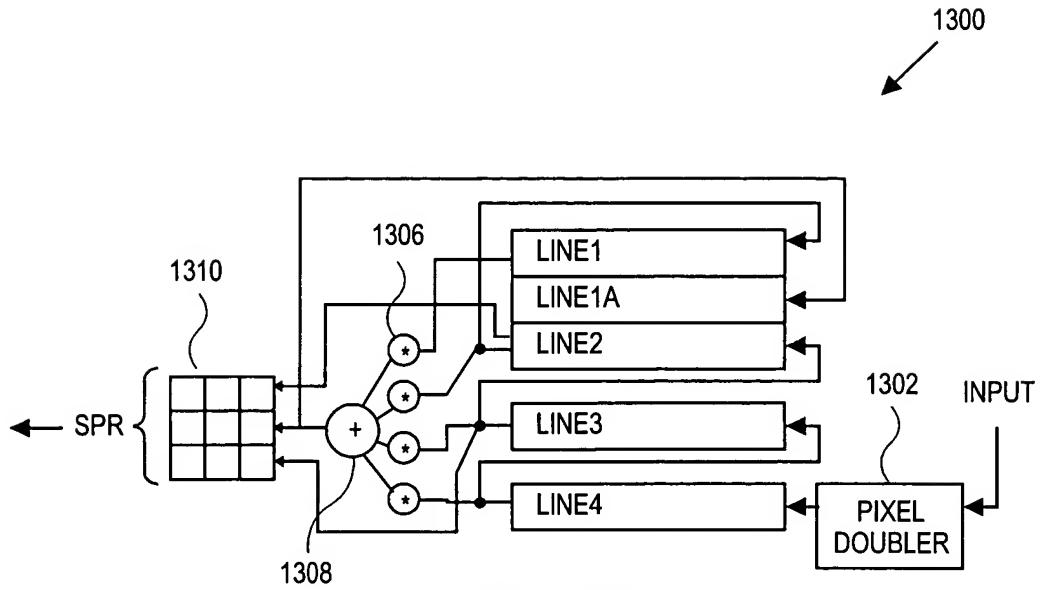


FIG. 13

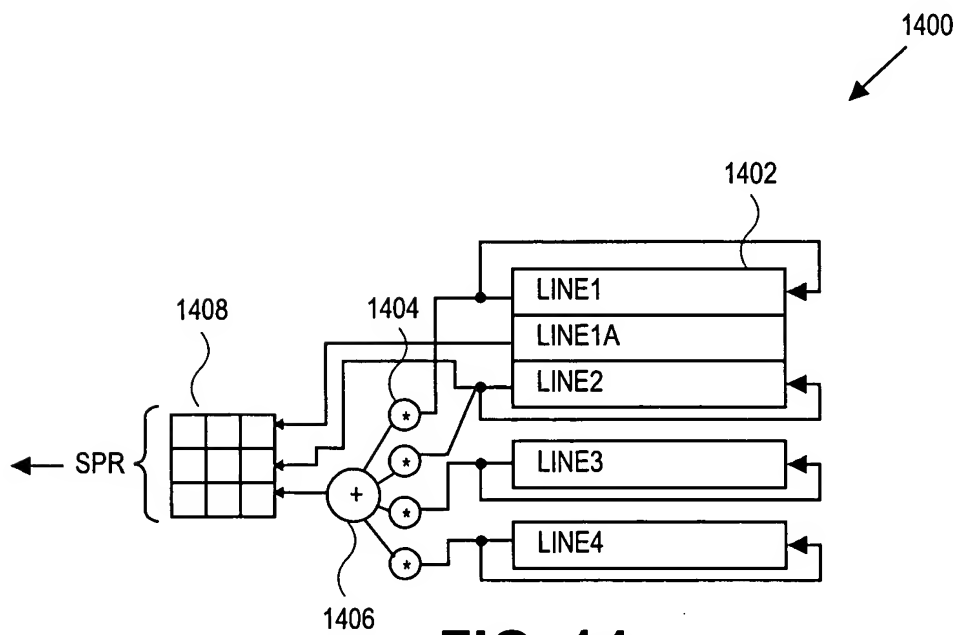


FIG. 14

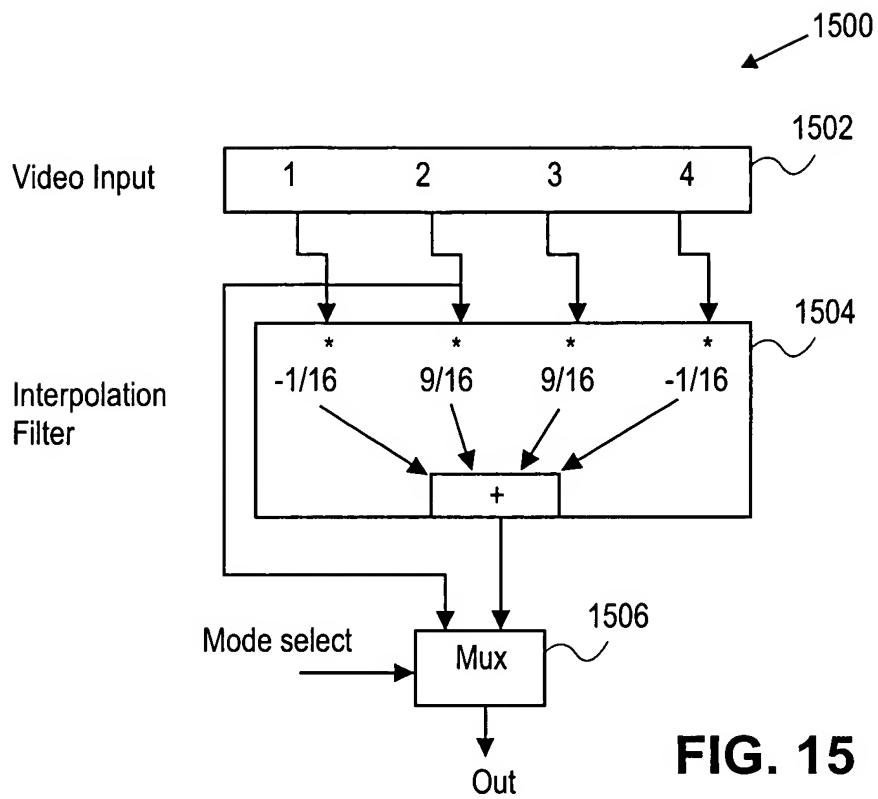


FIG. 15

Interpolation mode	1	I1	2	I2	3	I3	4	I4	5	I5
Duplication mode	1	1	2	2	3	3	4	4	5	5

FIG. 16

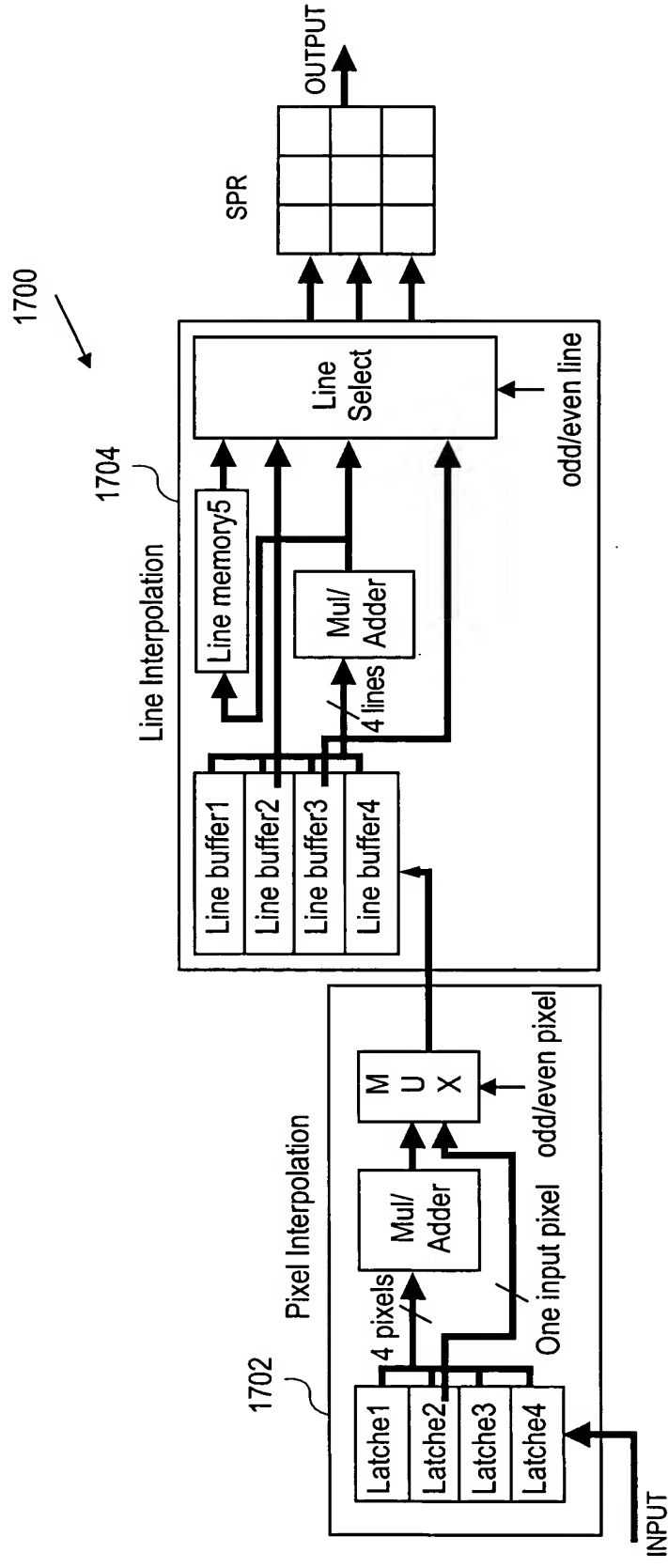


FIG. 17

1800

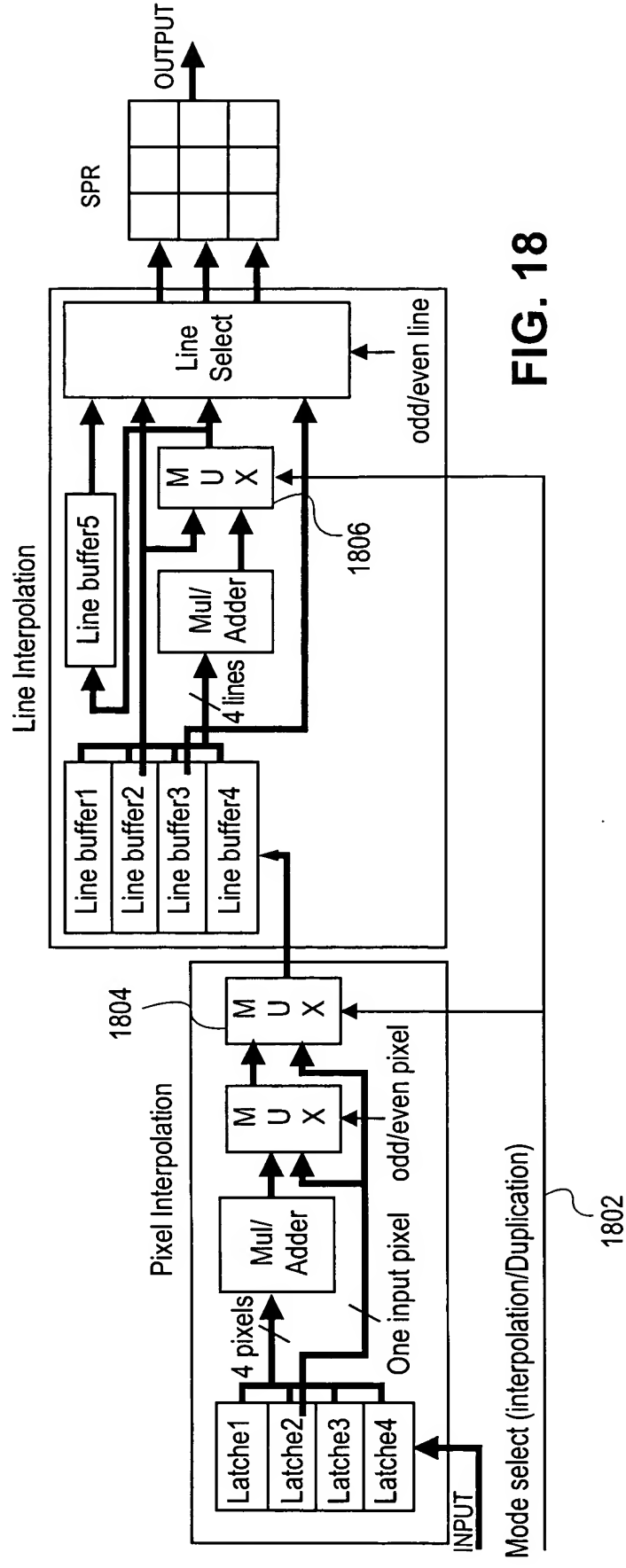


FIG. 18

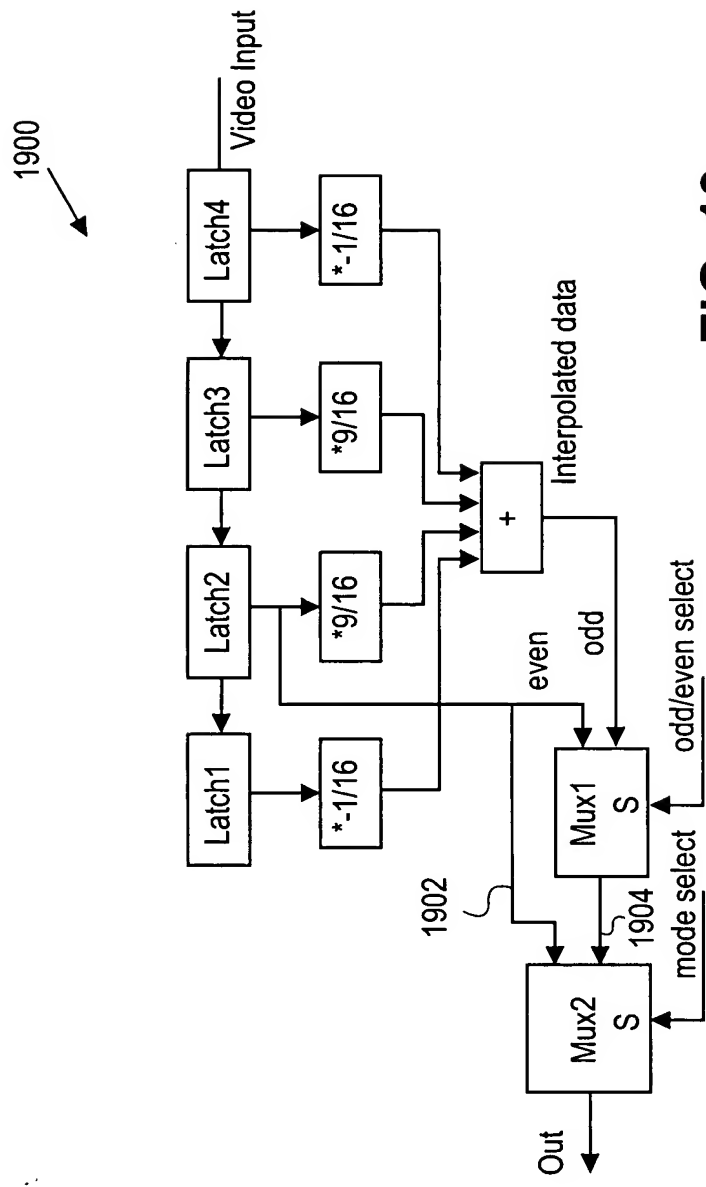


FIG. 19

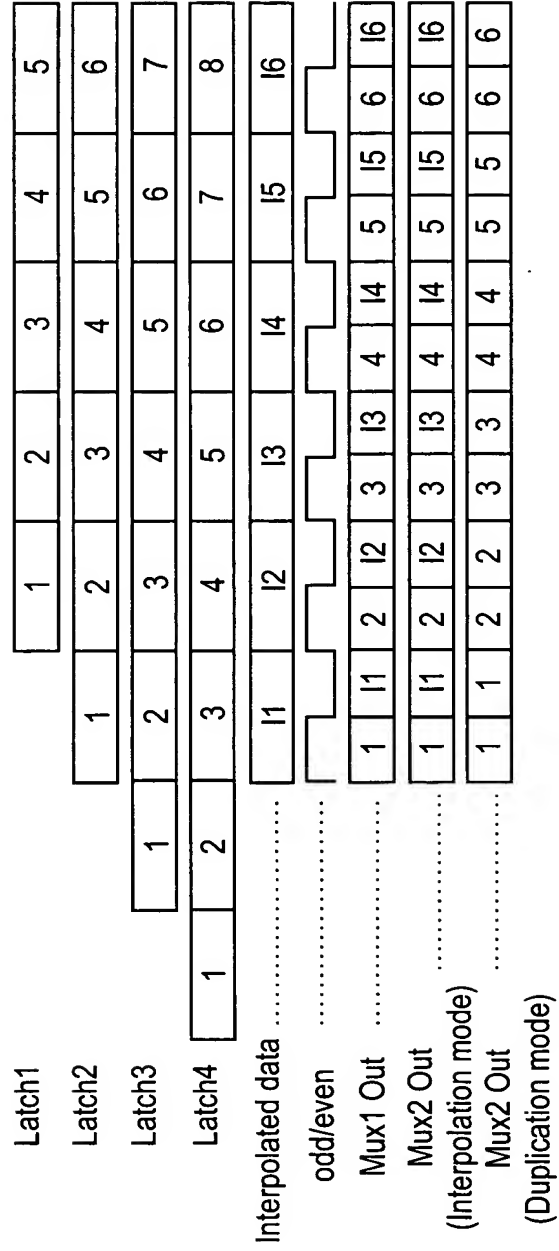


FIG. 20

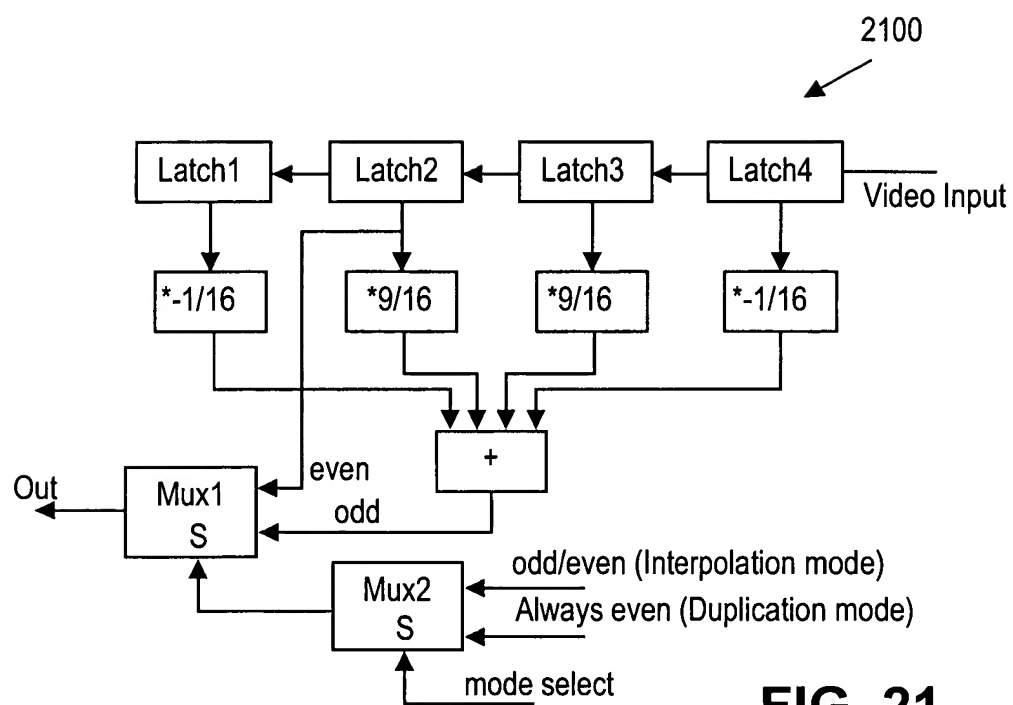


FIG. 21

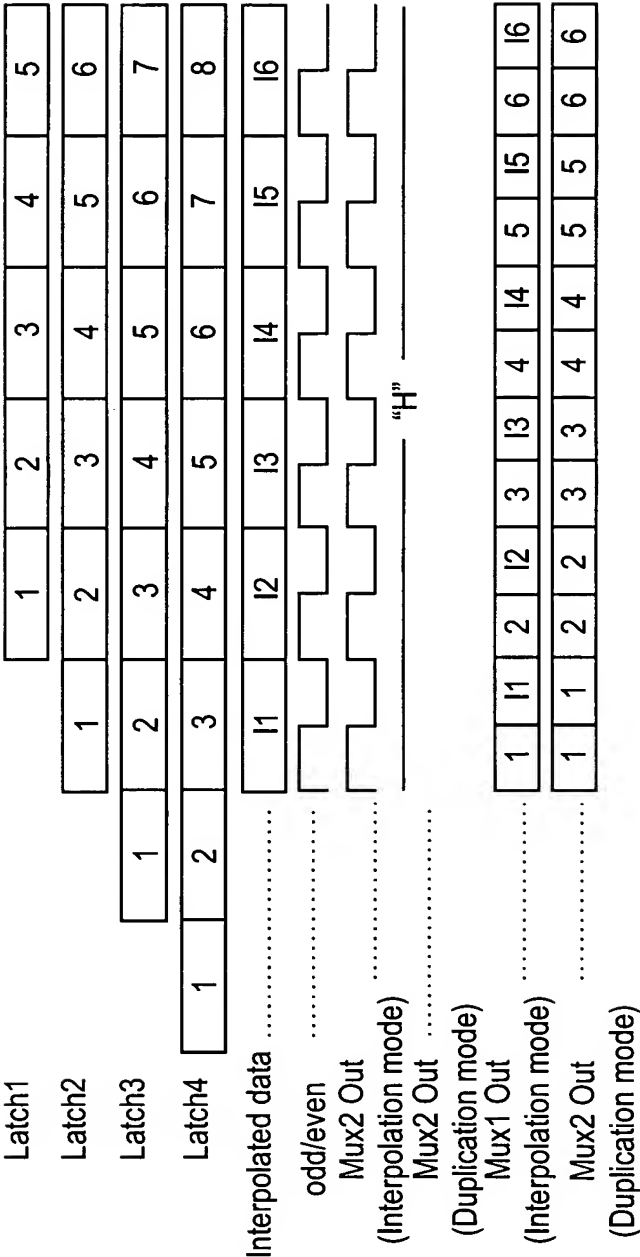


FIG. 22

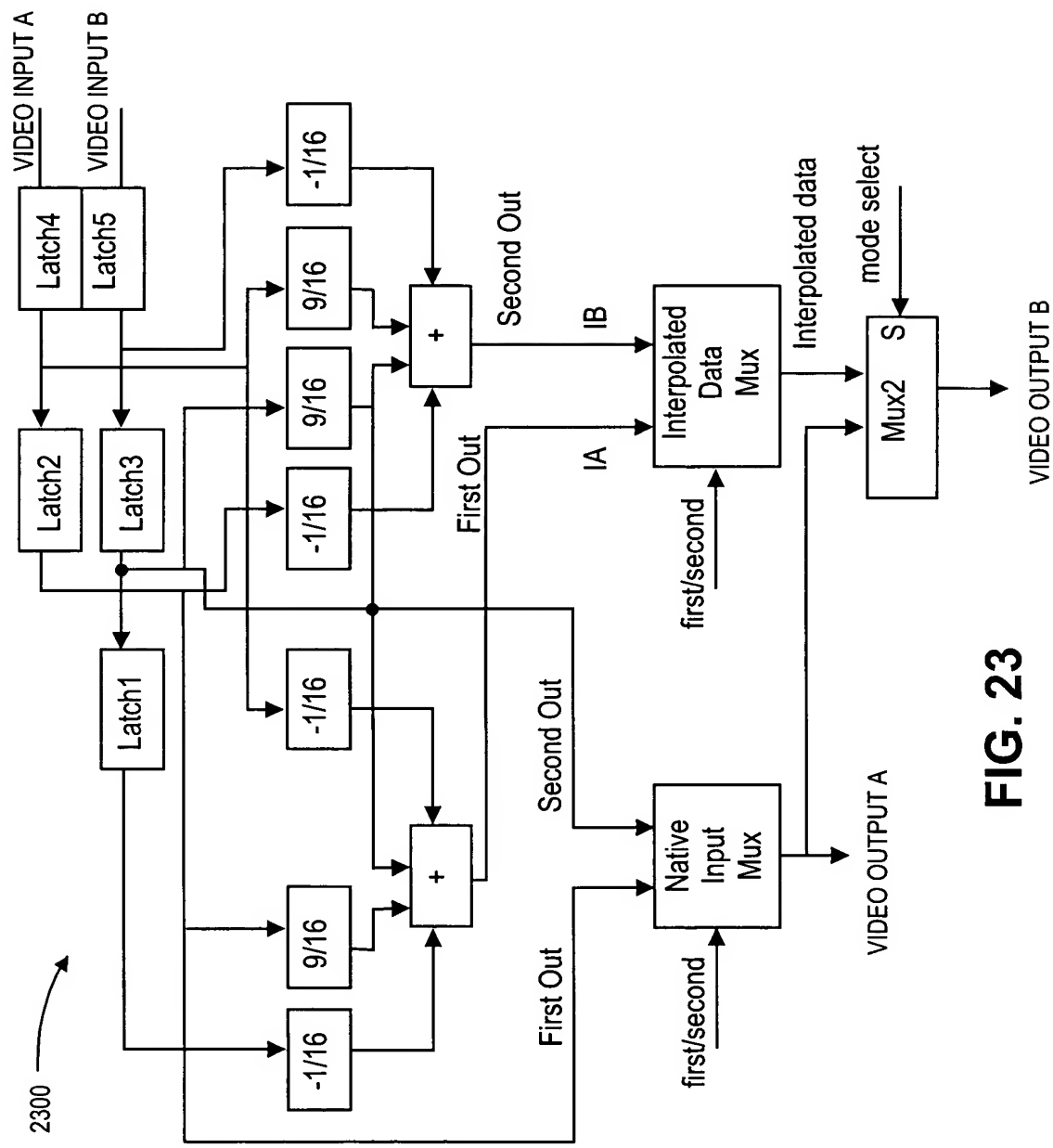


FIG. 23

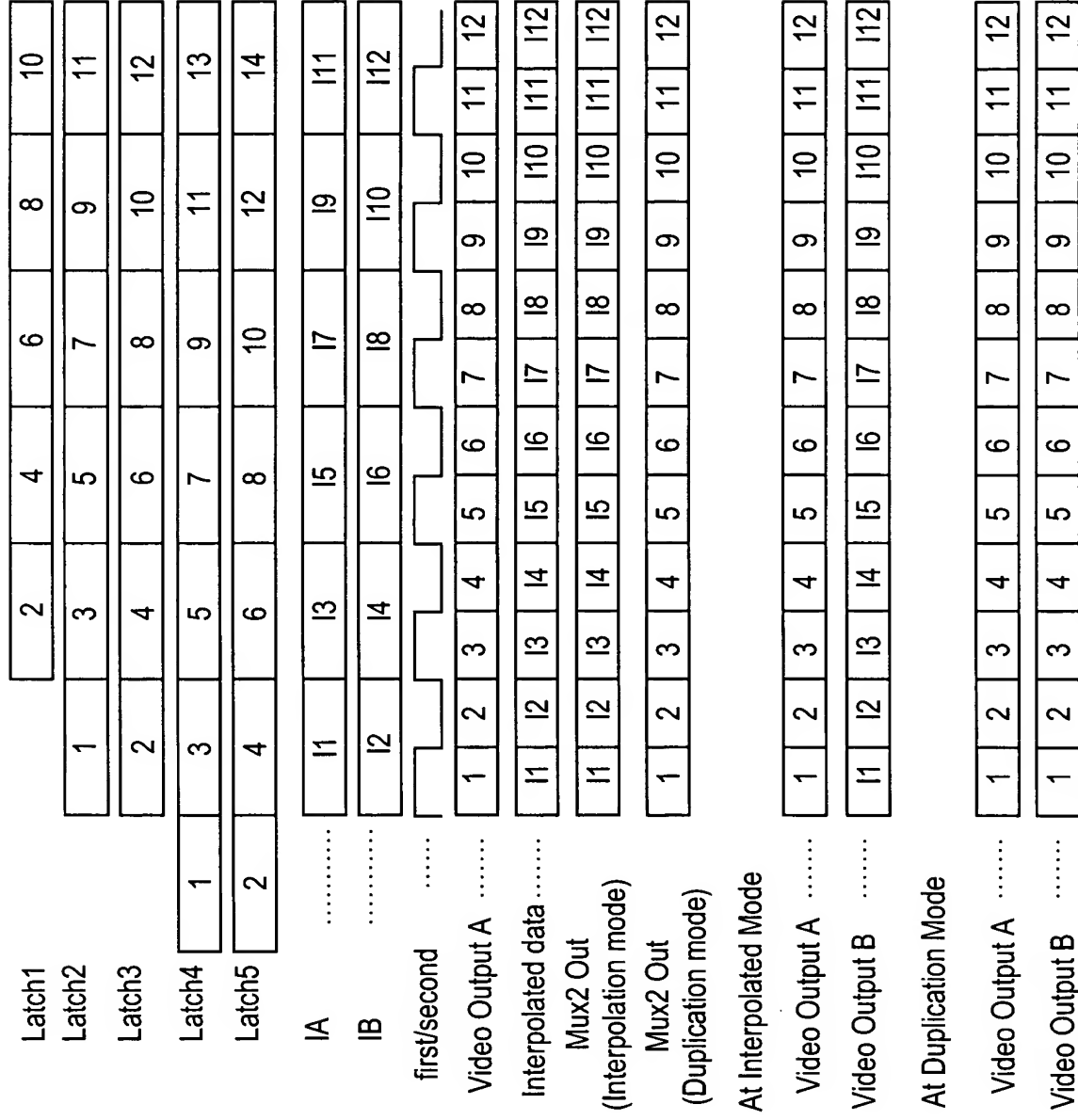


FIG. 24

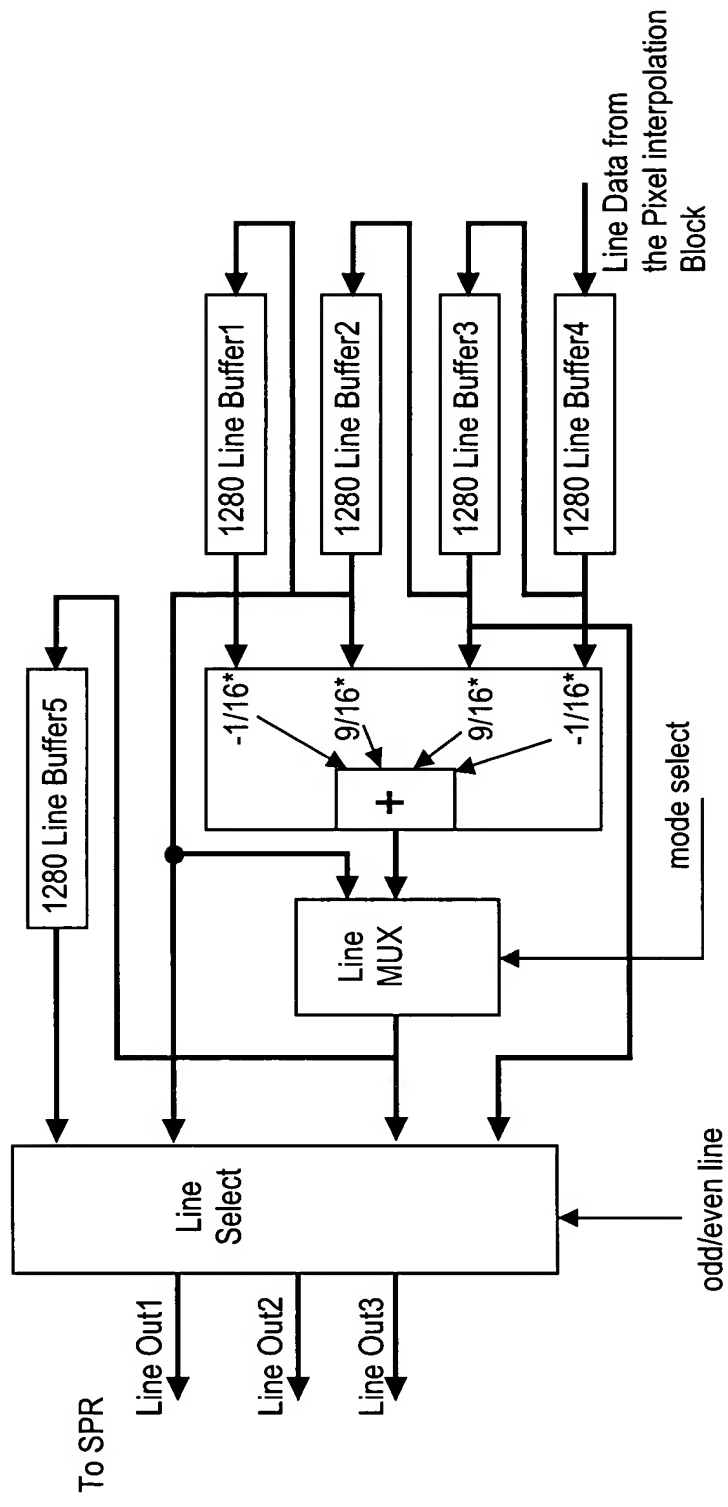


FIG. 25

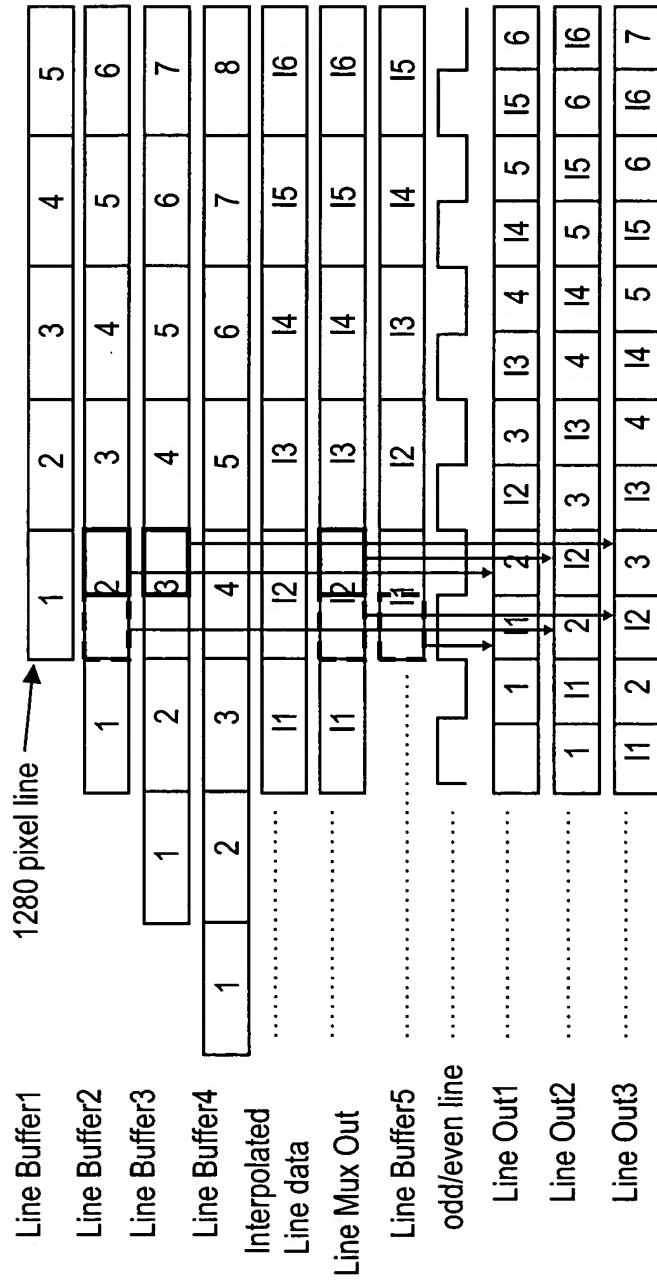


FIG. 26

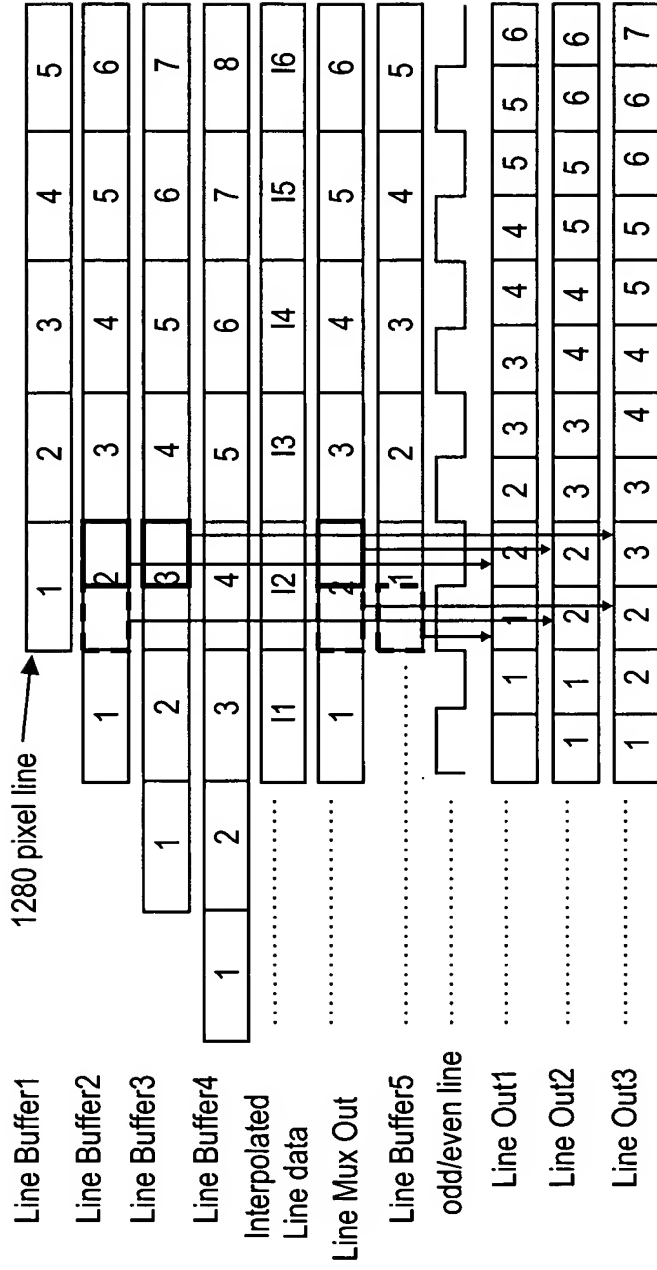


FIG. 27

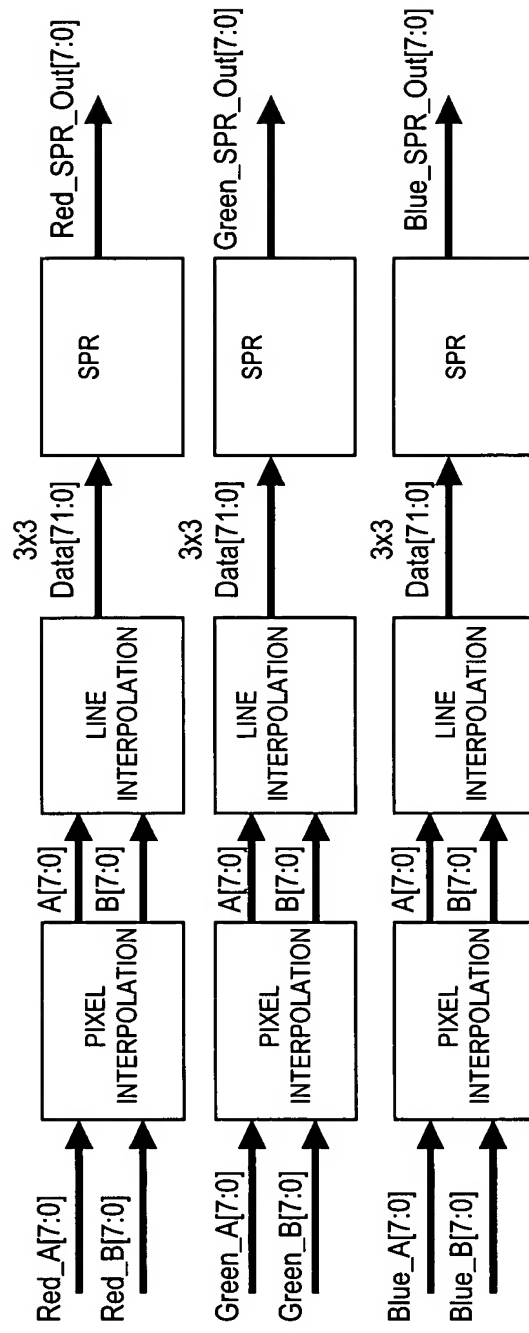


FIG. 28

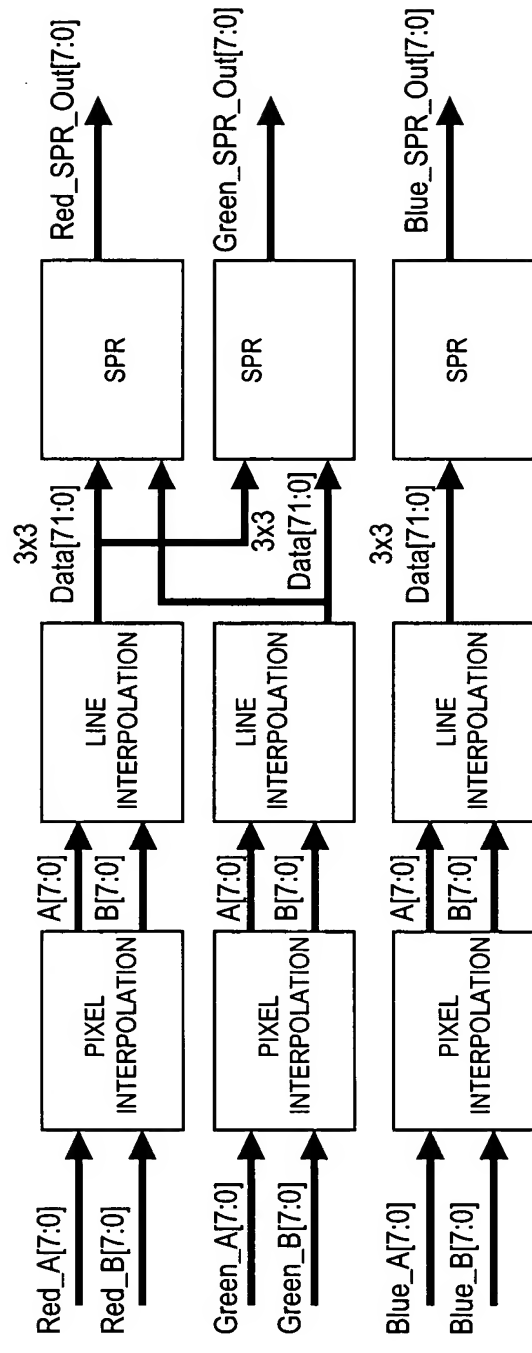


FIG. 29

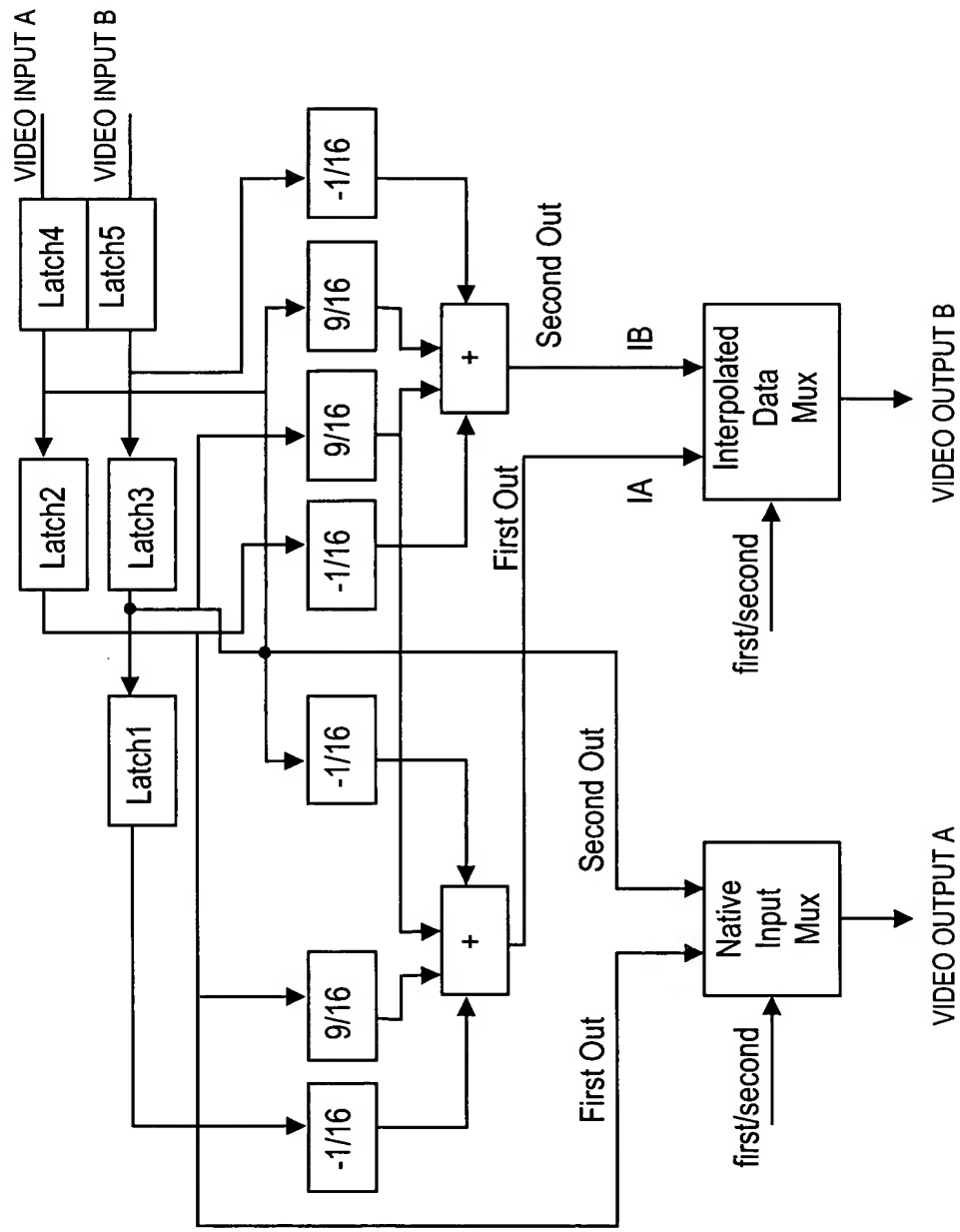
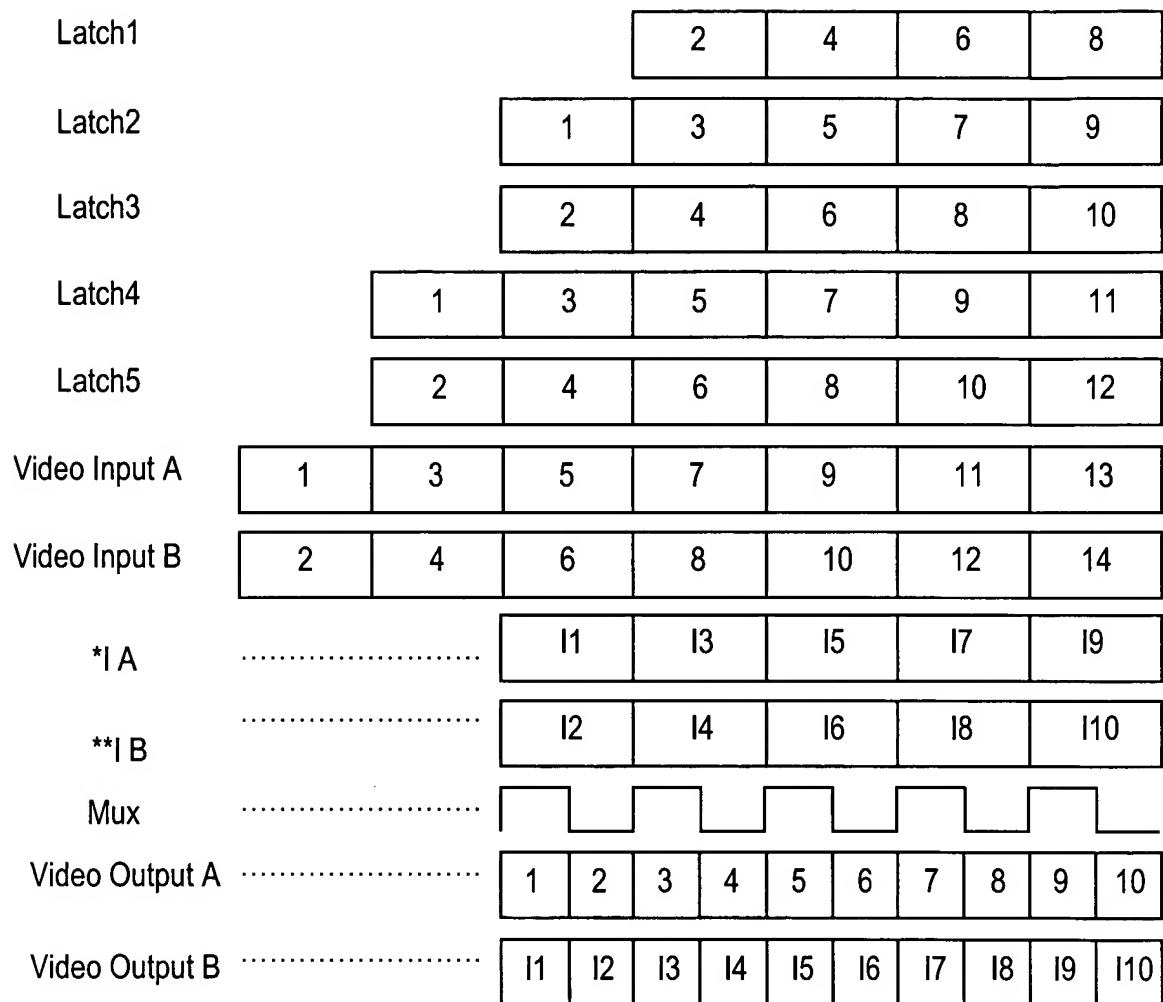


FIG. 30



* I A = Latch1*(-1/16) + Latch2*(9/16) + Latch3*(9/16) + Latch4*(-1/16)

** I B = Latch2*(-1/16) + Latch3*(9/16) + Latch4*(9/16) + Latch5*(-1/16)

FIG. 31

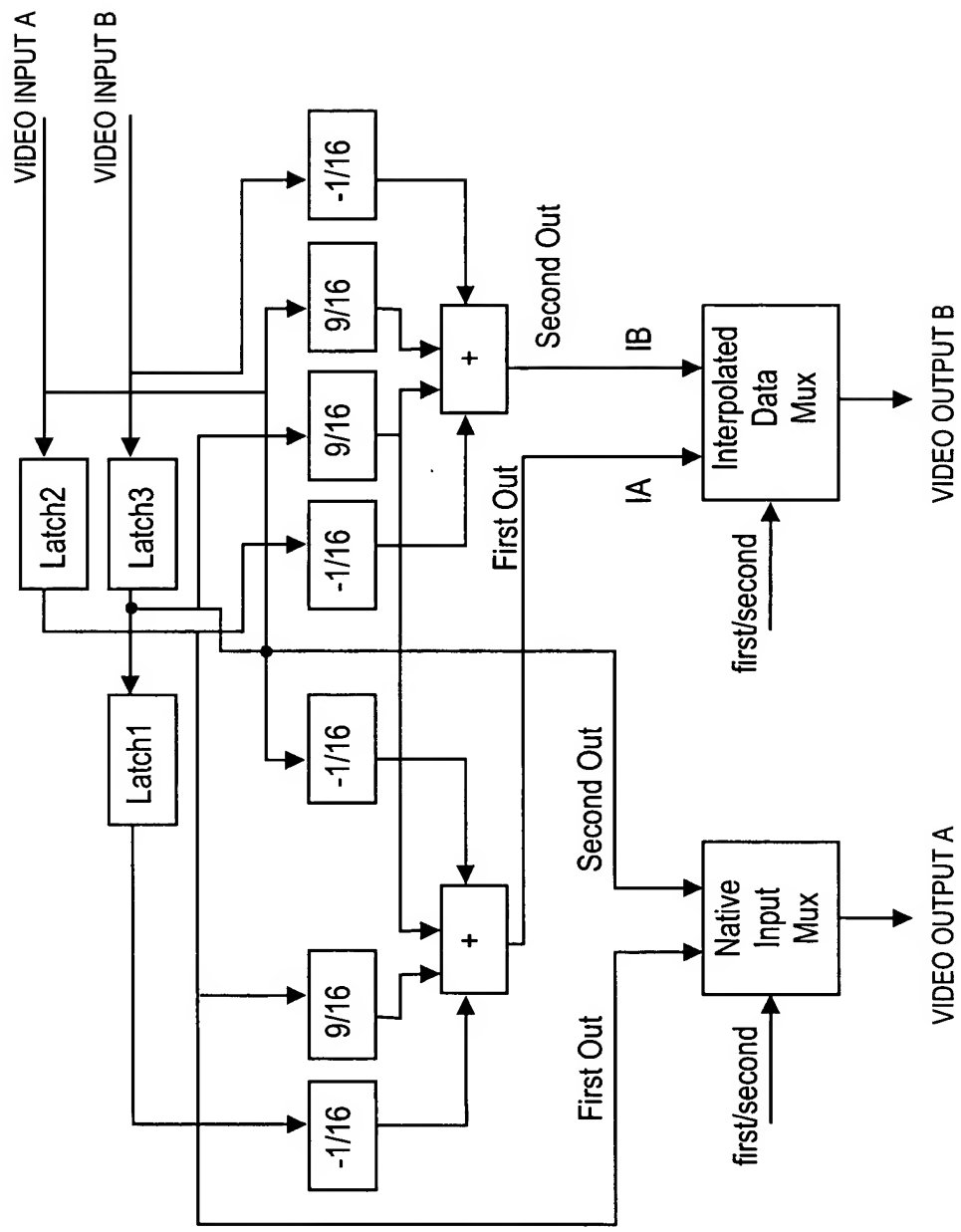
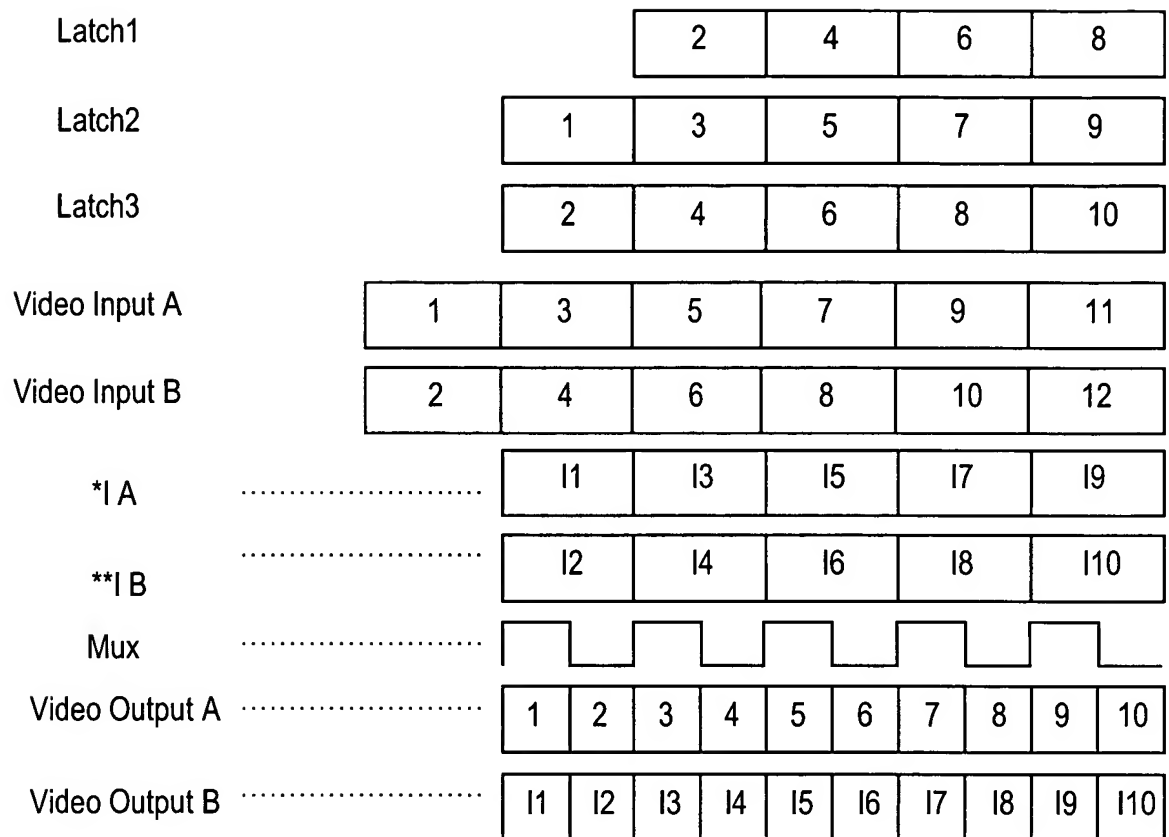


FIG. 32



$$*IA = \text{Latch1} * (-1/16) + \text{Latch2} * (9/16) + \text{Latch3} * (9/16) + \text{Video Input A} * (-1/16)$$

$$**IB = \text{Latch2} * (-1/16) + \text{Latch3} * (9/16) + \text{Video Input A} * (9/16) + \text{Video Input A} * (-1/16)$$

FIG. 33

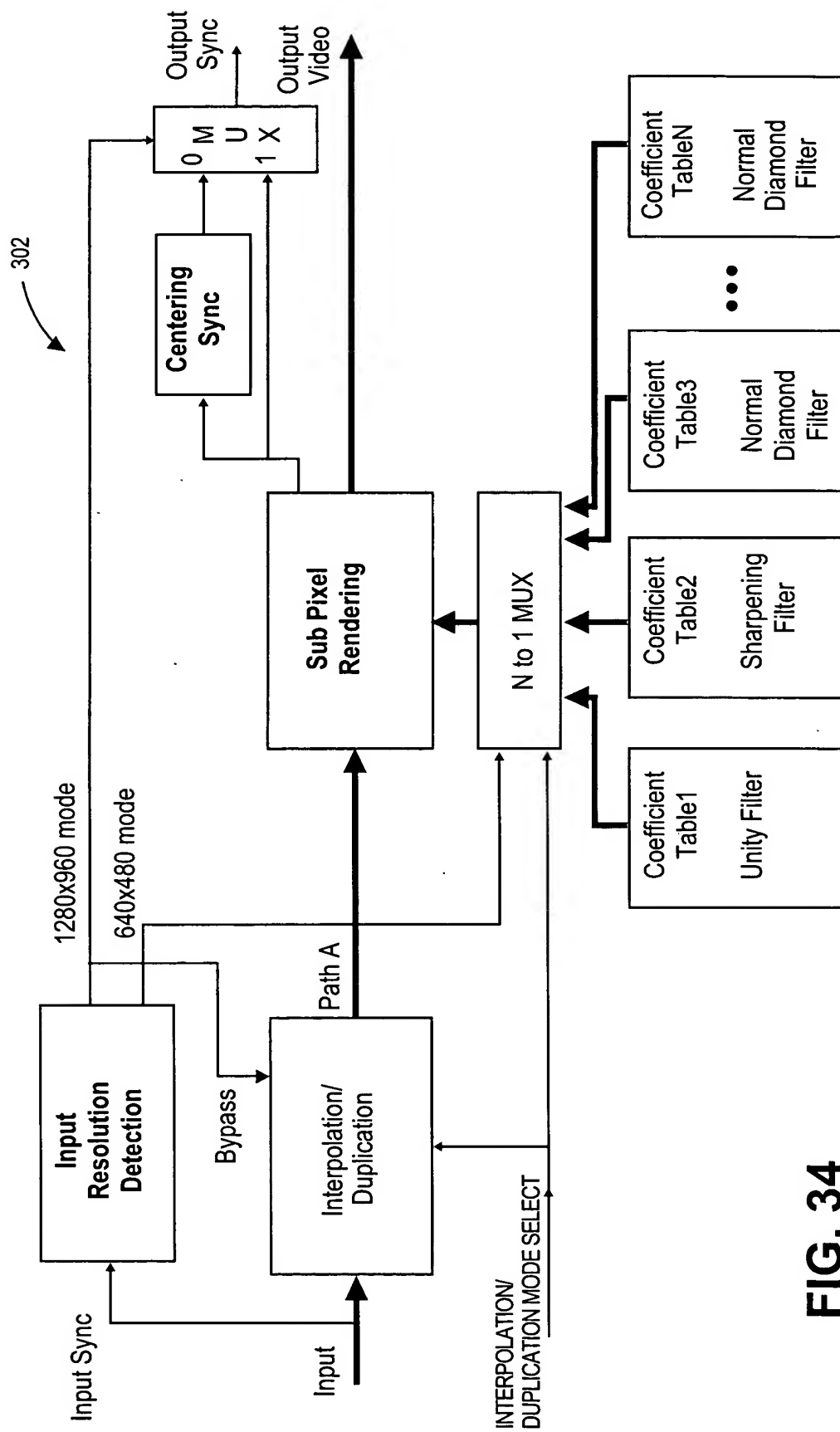


FIG. 34

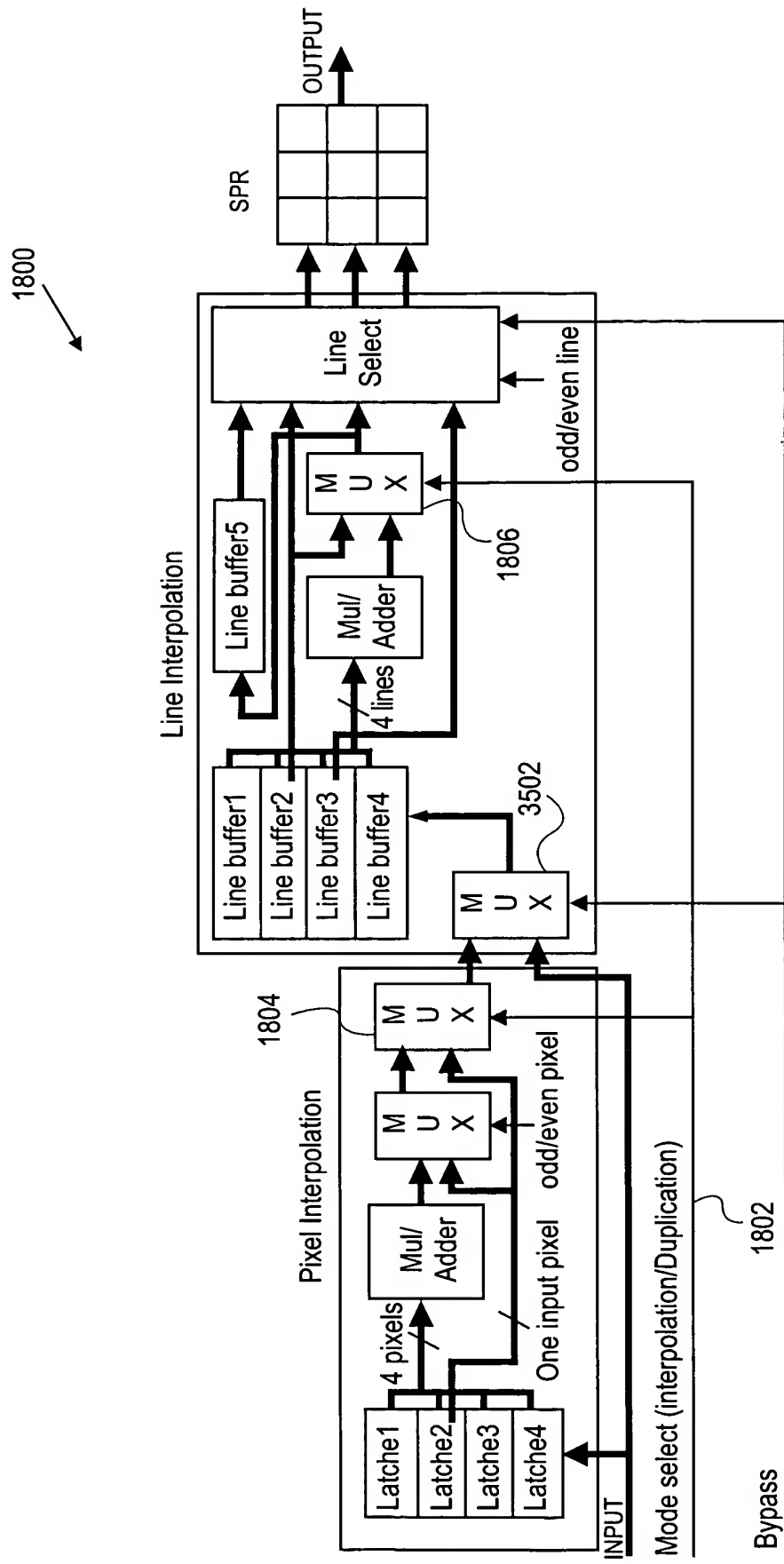


FIG. 35

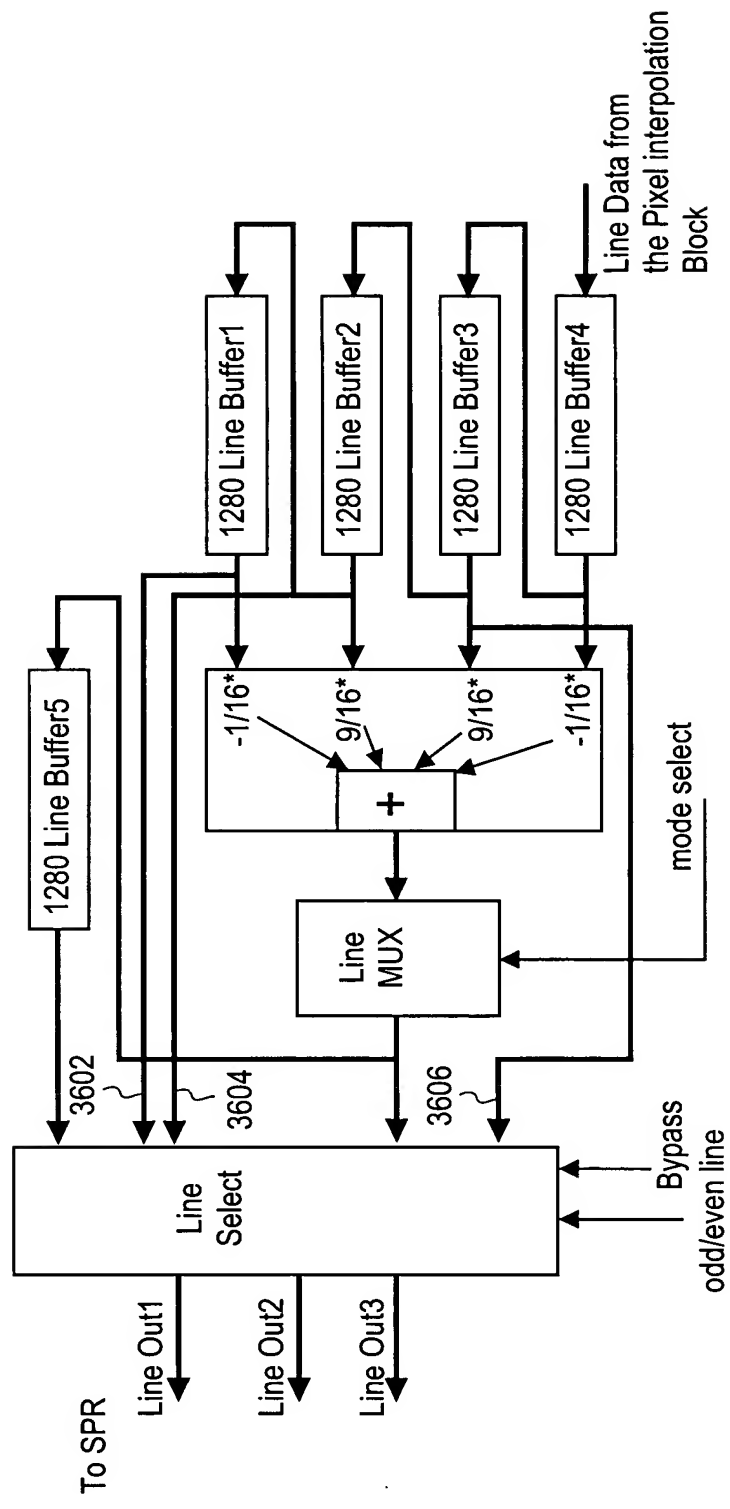


FIG. 36

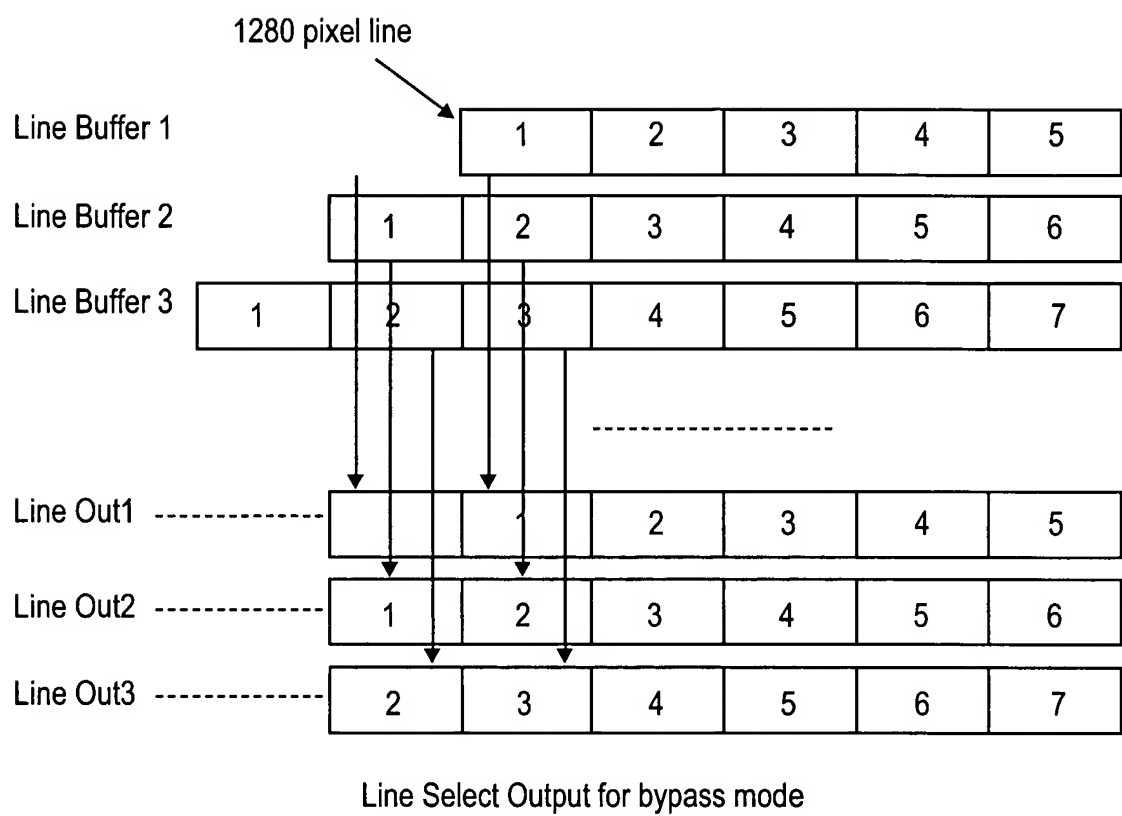


FIG. 37

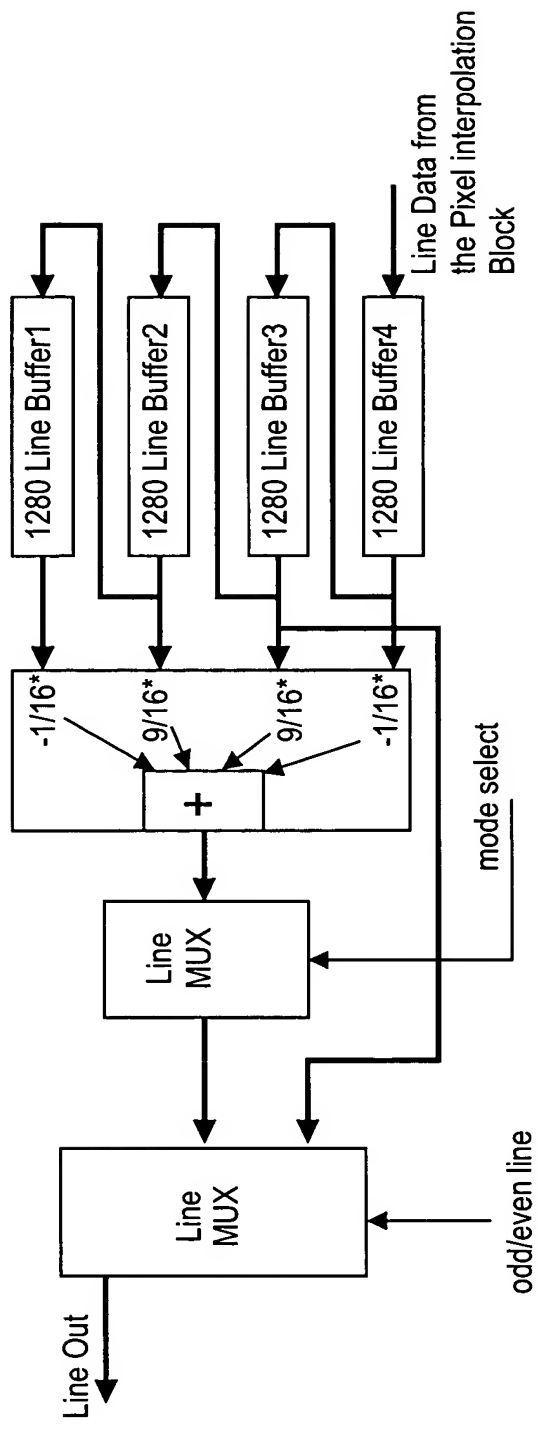


FIG. 38

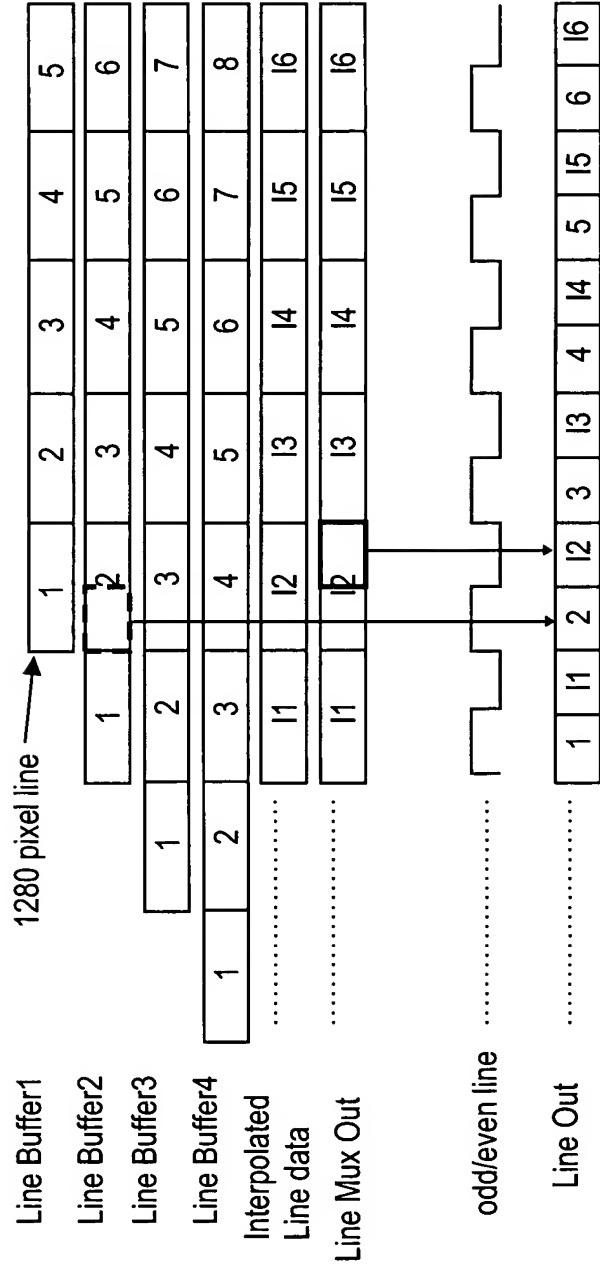


FIG. 39

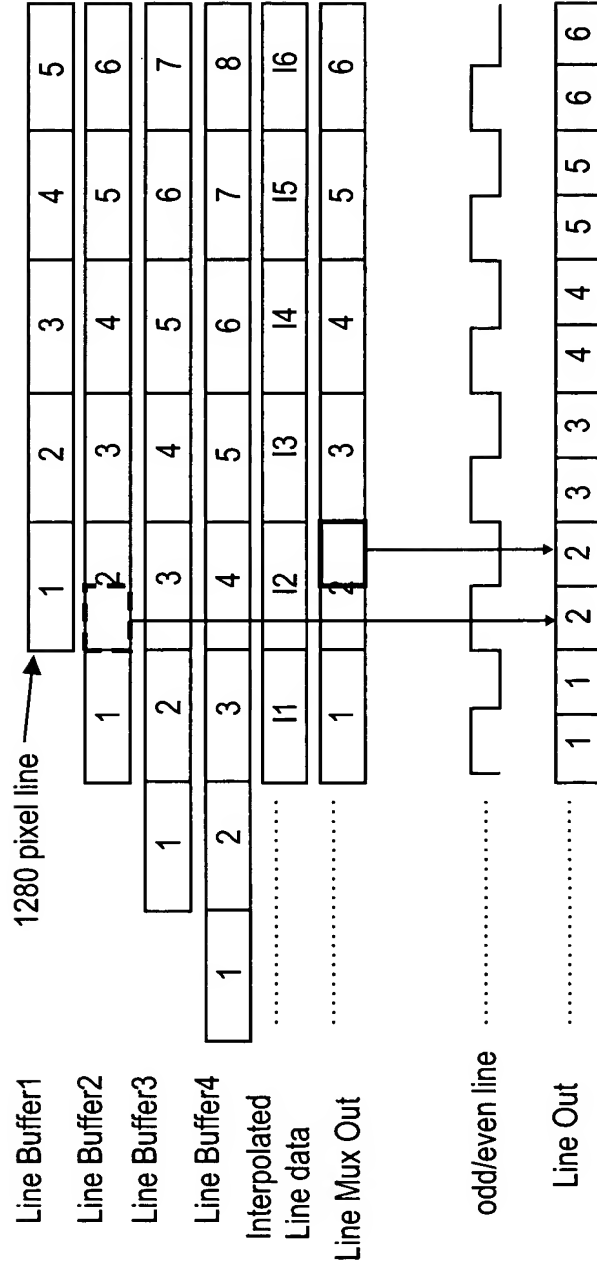


FIG. 40